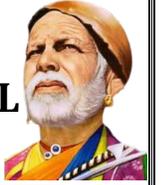




Hindi Seva Mandal's (Estd. 1950)
SHRI SANT GADGE BABA
COLLEGE OF ENGINEERING AND TECHNOLOGY, BHUSAWAL
AN AUTONOMOUS INSTITUTE
ACCREDITED BY NAAC WITH A+ Grade



TECHZEALOT - 2026
EVENT: PC ASSEMBLING
RULES AND MARKING SCHEME

| SR. NO. | NAME OF CONNECTION | ALLOTTED POINTS |
|-----------------------|---|------------------------|
| 1 | MOTHERBOARD POWER CONNECTION | 2 |
| 2 | RAM CONNECTION | 1 |
| 3 | CMOS BATTERY CONNECTION | 1 |
| 4 | PROCESSOR LOCKING SYSTEM | 4 |
| 5 | FAN POWER CONNECTION | 1 |
| 6 | HARD DRIVE , FLOPPY DRIVE & CD-ROM POWER CONNECTION | 3 |
| 7 | FLOPPY DRIVE DATA CABLE CONNECTION | 2 |
| 8 | HARD DRIVE IDE CONNECTION | 1 |
| 9 | CD-ROM IDE CONNECTION | 1 |
| 10 | FRONT PANEL LED / SWITCH CONNECTION | 4 |
| TOTAL POINTS:- | | 20 |

Rules:

1. Max **Time Limit is 2 minutes** to connect all the above components.
2. Candidate securing **maximum points in minimum time limit will be shortlisted** after each round and result will be updated on RESULT BOARD accordingly.

PC-ASSEMBLING
EVENT INCHARGE
Prof. R. P. CHAUDHARI