Hindi Seva Mandal's (Estd. - 1950)

SHRI SANT GADGE BABA COLLEGE OF ENGINEERING & TECHNOLOGY, BHUSAWAL – 425 203 (M. S.) TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION – II

Session: 2017-18 SEM - I Date: 11/09/2017

Programme of S. E. (All Branches) Examinations:

- 1. Candidates should write answer sheets with BLACK INK only
- 2. Candidates are required to be present at their respective places of the Examinations fifteen minutes before the time of the paper.
- 3. Candidates are forbidden to take any Study material into Examination Hall, with a view to prevent malpractices at the time of examinations.
- 4. Candidates should see Notice-Board at their respective places of examination regularly, for changes if any notified later, in the programme.
- 5. Candidates are instructed to note the date, day, time and place of every paper on every day.
- 6. Candidates are permitted to use stencils at the time of examinations.
- 7. The exchange or loan of slide-rules, drawing instruments or other materials used in the examination, is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
- 8. Use of non-programmable, battery operated Electronic Calculator of pocketsize is allowed. The Exchange of Calculator is not allowed.
- 9. The written examinations will be conducted in the following order...

***** TIME TABLE *****

BRANCH	DATE: 27/09/2017	DATE: 28/09/2017	DATE: 29/09/2017
S.E.(Civil)	Engineering Mathematics - III	Concrete Technology	Surveying 1
S.E. (Mechanical)	Fluid Mechanics	Strength of Materials	Manufacturing Engineering - 1
S.E. (E. & T. C.)	Solid State Devices & Circuits-1	Digital Techniques & Applications	Communication Systems - I
S.E. (Electrical)	Engineering Mathematics - III	Electrical Measurement – I	Electrical Engineering Materials
S.E. (Computer)	Engineering Mathematics - III	Discrete Structure & Graph Theory	Object Oriented Technology

SUBJECT & TIME - 02.30 PM. TO 03.30 PM. (AFTERNOON SESSION)

BRANCH	DATE: 27/09/2017	DATE: 28/09/2017	DATE: 29/09/2017
S. E.(Civil)	Strength of Materials	Building Construction Techniques & Materials	-
S.E. (Mechanical)	Engineering Thermodynamics	Material Science and Metallurgy	#
S.E. (E. & T. C.)	Electrical Circuits and Machines	Component Devices & Instrumentation Technology	-
S.E. (Electrical)	Power Plant Engineering	Power System – I	
S.E. (Computer)	Analog & Digital Electronics	Microprocessor & Microcontroller	

Prof. D. D. Patil

Deputy Examination Controller

Prof S B Oiba

Prof. S. B. Ojha Examination Controller

Hindi Seva Mandal's (Estd. - 1950)

SHRI SANT GADGE BABA COLLEGE OF ENGINEERING & TECHNOLOGY, BHUSAWAL - 425 203 (M. S.) TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION - II Date: 11/09/2017

Session: 2017-18

SEM - I

Programme of T. E. (All Branches) Examinations:

- 1) Candidates should write answer sheets with BLACK INK only.
- 2) Candidates are required to be present at their respective places of the Examinations fifteen minutes before the time of the paper.
- 3) Candidates are forbidden to take any Study material into Examination Hall, with a view to prevent malpractices at the time of examinations.
- 4) Candidates should see Notice-Board at their respective places of examination regularly, for changes if any notified later, in the programme.
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- 8) Use of non-programmable, battery operated Electronic Calculator of pocketsize is allowed. The Exchange of Calculator is not allowed.
- 9) The written examinations will be conducted in the following order...

* TIME TABLE *

		Thur Trace	PESSION
	SUBJECT & TIM	E - 12.00 NOON TO 01.00 PM. (MORNING S	DATE: 29/09/2017
BRANCH	DATE: 27/09/2017	DATE: 28/09/2017	Construction Management - 1
T.E.(Civil)	Structural Design - 1	Fluid Mechanics – II	Industrial Safety & Engineering
T.E. (Mechanical)	Heat Transfer	Machine Design - I	and the same of th
T.E. (E. & T. C.)	Electronic Circuit Design	Microcontrollers & Peripheral Interface Controller	Industrial Organization & Management
T.E. (Electrical)	Electrical Machines - II	Power Electronics	Principles of Management
T.E. (Computer)	Software Engineering	Computer Network	rincipes of the second

SUBJECT & TIME - 04:00 PM. TO 05.00 PM. (AFTERNOON SESSION)

		DATE: 28/09/2017	DATE: 29/09/2017
BRANCH	DATE: 27/09/2017	Environmental Engineering - I	
A A MARK THE PARTY OF THE PARTY	Infrastructural Engineering - I	Theory of Machine - II	-
I a fine fine and the second	Internal Combustion Engine	Feedback Control System	-
I . Las Takes on	Communication System 17	Electromagnetic Engineering	-
I also (assessed	Power System - II Formal Language & Automata Theory	THE RESIDENCE OF THE PARTY OF T	-
T.E. (Computer)	Formal Language & Automata Theory	Toyana in the	all the

Prof. D. D. Patil Deputy Examination Controller

Prof. S. B. Ojha **Examination Controller** Hindi Seva Mandal's (Estd. - 1950)

SHRI SANT GADGE BABA COLLEGE OF ENGINEERING & TECHNOLOGY, BHUSAWAL – 425 203 (M. S.) TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION – II

Session: 2017-18 SEM - 1 Date: 11/09/2017

Programme of B. E. (All Branches) Examinations:

- 1) Candidates should write answer sheets with BLACK INK only.
- 2) Candidates are required to be present at their respective places of the Examinations fifteen minutes before the time of the paper.
- 3) Candidates are forbidden to take any Study material into Examination Hall, with a view to prevent malpractices at the time of examinations.
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- 8) Use of non-programmable, battery operated Electronic Calculator of pocketsize is allowed. The Exchange of Calculator is not allowed.
- 9) The written examinations will be conducted in the following order...

***** TIME TABLE *****

BRANCH	DATE: 27/09/2017	DATE: 28/09/2017	DATE: 29/09/2017
B.E.(Civil)	Water Resources Engineering - I	Software Engineering & Project Management	Geotechnical Engineering - II
B.E. (Mechanical)	Refrigeration & Air Conditioning	Renewable Energy Sources	Operation Research
B.E. (E. & T. C.)	Digital Signal Processing	Renewable Energy Sources	Computer Communication Network
B.E. (Electrical)	Industrial Drives & Control	Energy Resources & Technology	Power System Operation & Control
B.E. (Computer)	Advanced Unix Programming	Geographical Information systems	Embedded System

SUBJECT & TIME - 04:00 PM. TO 05.00 PM. (AFTERNOON SESSION)

BRANCH	DATE: 27/09/2017	DATE: 28/09/2017	DATE: 29/09/2017
B.E.(Civil)	Estimating and Costing	Watershed Management	
B.E. (Mechanical)	Computer Aided Design and Computer Aided Manufacturing	Automobile Engineering – I	
B.E. (E. & T. C.)	Fiber Optic Communication	VLSI Design	
B.E. (Electrical)	High Voltage Engineering	Industrial Electrical Engineering	
B.E. (Computer)	Artificial Intelligence & Expert System	Android Programming	120
			11/10

Prof. D. D. Patil

Deputy Examination Controller

Prof. S. B. Ojha Examination Controller