

Hindi Seva Mandal's (Estd. 1950's)

SHRI SANT GADGE BABA COLLEGE OF ENGINEERING & TECHNOLOGY, BHUSAWAL – 425 203 (M. S.)

TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION – II

Session: 2016 - 2017

SEMESTER - II

Date: 16/03/2017

Programme of S. E. (All Branches) Examinations:

1. Candidates should write answer sheets with **BLACK INK** only.
2. Candidates are required to be present at their respective places of the Examinations fifteen minutes before the time of the paper.
3. Candidates are forbidden to take any Study material into Examination Hall, with a view to prevent malpractices at the time of examinations.
4. Candidates should see Notice-Board at their respective places of examination regularly, for changes if any notified later, in the programme.
5. Candidates are instructed to note the date, day, time and place of every paper on every day.
6. Candidates are permitted to use stencils at the time of examinations.
7. The exchange or loan of slide-rules, drawing instruments or other materials used in the examination, is not permitted while the examination is in progress. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
8. Use of non-programmable, battery operated Electronic Calculator of pocketsize is allowed. The Exchange of Calculator is not allowed.
9. The written examinations will be conducted in the following order...

******* TIME TABLE *******

SUBJECT & TIME - 10.30 AM. TO 11.30 AM. (MORNING SESSION)			
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017	DATE: 01/04/2017
S.E.(Civil)	Engineering Geology	Theory of Structures – I	Surveying - II
S.E. (Mechanical)	Engineering Mathematics - III	Applied Thermodynamics	Manufacturing Engineering - II
S.E. (E. & T. C.)	Engineering Mathematics - III	Microprocessors	Network Analysis & Synthesis
S.E. (Electrical)	Analog and Digital Electronics	Electrical Machine – I	Numerical Techniques
S.E. (Computer)	Data Communication	Data Structure	Computer Graphics
SUBJECT & TIME - 02.30 PM. TO 03.30 PM. (AFTERNOON SESSION)			
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017	DATE: 01/04/2017
S. E.(Civil)	Fluid Mechanics – I	Building Design & Drawing	--
S.E. (Mechanical)	Theory of Machines – I	Basic Electrical drives and Control	--
S.E. (E. & T. C.)	Solid State Devices and Circuits - II	Linear Integrated Circuits	--
S.E. (Electrical)	Network Analysis	Electrical Installation Estimation & Distribution	--
S.E. (Computer)	Microprocessor & Microcontroller Interfacing	Computer Organization	--

Prof. D. D. Patil
Deputy Examination Controller

Prof. S. B. Ojha
Examination Controller

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TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION – II

Session: 2016 - 2017

SEMESTER - II

Date: 16/03/2017

Programme of T. E. (All Branches) Examinations:

- 1) Candidates should write answer sheets with **BLACK INK** only.
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- 4) Candidates should see Notice-Board at their respective places of examination regularly, for changes if any notified later, in the programme.
- 5) Candidates are instructed to note the date, day, time and place of every paper on every day.
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- 9) The written examinations will be conducted in the following order...

******* TIME TABLE *******

SUBJECT & TIME - 12.00 NOON TO 01.00 PM. (MORNING SESSION)			
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017	DATE: 01/04/2017
T.E.(Civil)	Structural Design – II	Geotechnical Engineering – I	Construction Management – II
T.E. (Mechanical)	Machine Design – II	Metrology and Quality Control	Project and Business Management
T.E. (E. & T. C.)	Industrial Economics & Telecom Regulation	Electronic Measurement	Industrial Management
T.E. (Electrical)	Control System – I	Electrical Machine Design	Entrepreneurship Development
T.E. (Computer)	Operating System	DataBase Management System	Management of Information System
SUBJECT & TIME – 04:00 PM. TO 05.00 PM. (AFTERNOON SESSION)			
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017	DATE: 01/04/2017
T.E.(Civil)	Theory of Structure – II	Infrastructural Engineering – II	--
T. E. (Mechanical)	Numerical Analysis & Computational Methods	Turbo Machinery	--
T.E. (E. & T. C.)	Power Electronics	Audio Video Engineering	--
T.E. (Electrical)	Electrical Measurement – II	Microprocessor & Microcontroller	--
T.E. (Computer)	Object Oriented Modeling & Design	Analysis and Design of Algorithm	--

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TIME TABLE FOR INTERNAL SESSIONAL EXAMINATION – II

Session: 2016 - 2017

SEMESTER - II

Date: 16/03/2017

Programme of B. E. (All Branches) Examinations:

- 1) Candidates should write answer sheets with **BLACK INK** only.
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******* TIME TABLE *******

SUBJECT & TIME - 12.00 NOON TO 01.00 PM. (MORNING SESSION)		
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017
B.E.(Civil)	Water Resources Engineering II	Construction Safety and Disaster Management
B.E. (Mechanical)	Mechanical Vibration	Process Equipment Design
B.E. (E. & T. C.)	Satellite and Mobile Communication	Digital Image Processing
B.E. (Electrical)	Power System Stability	Power System Design and Practice
B.E. (Computer)	Compiler Design	Cryptography & Network Security
SUBJECT & TIME – 04:00 PM. TO 05.00 PM. (AFTERNOON SESSION)		
BRANCH	DATE: 30/03/2017	DATE: 31/03/2017
B.E.(Civil)	Environmental Engineering II	Architecture and Town Planning
B.E. (Mechanical)	Finite Element Analysis and Simulation Techniques	Automobile Engineering – II
B.E. (E. & T. C.)	Radiation and Microwave Techniques	Nano Technology
B.E. (Electrical)	Switchgear and Protection	Flexible AC Transmission System and Power Quality
B.E. (Computer)	Data Warehousing and Mining	Mobile Computing

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